

図 1A

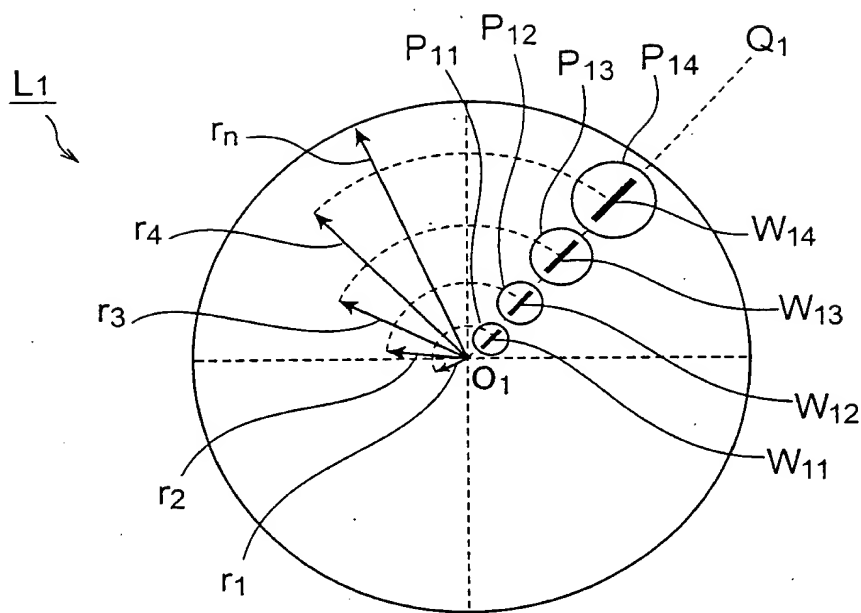


図 1B

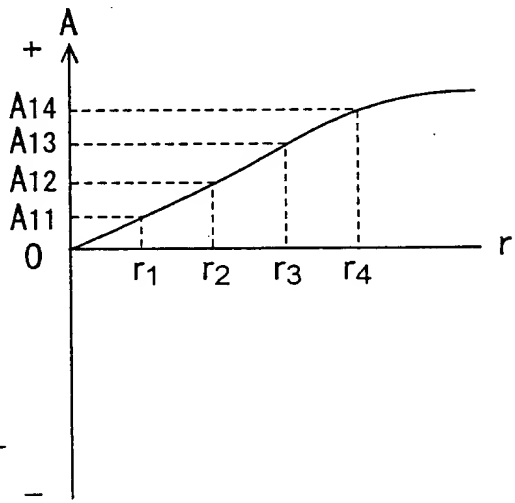


図2A

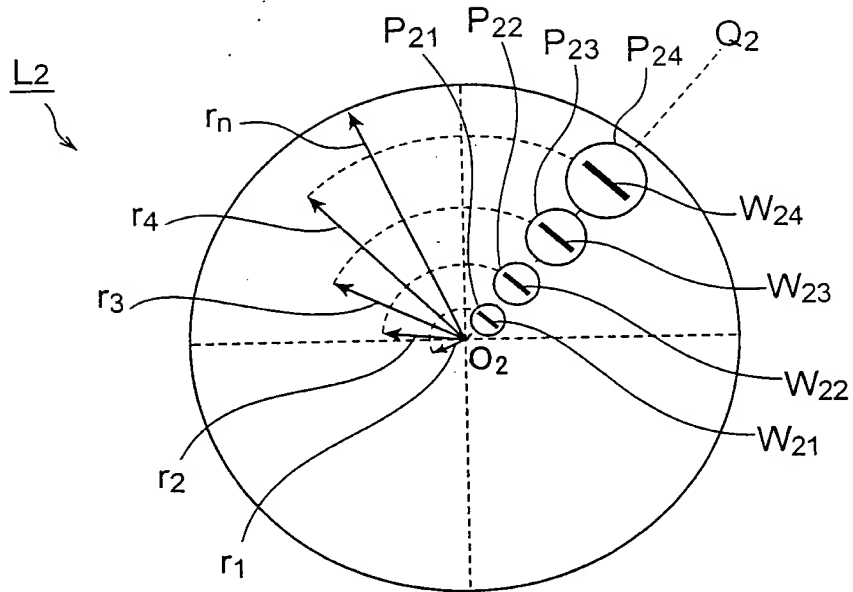


図2B

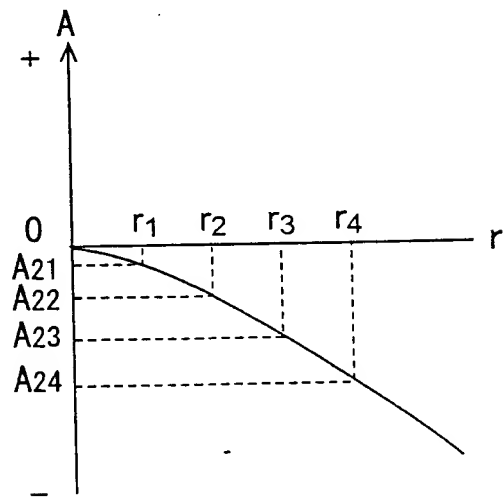


図3A

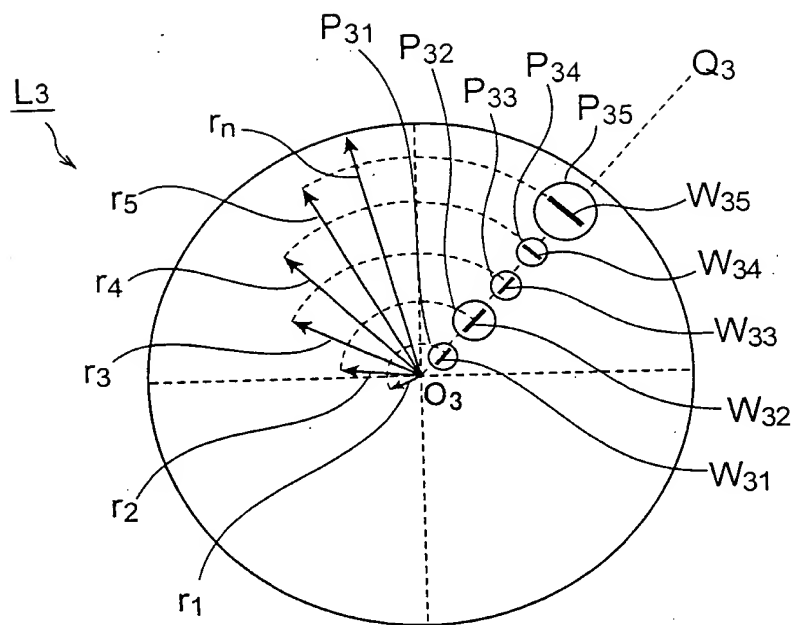


図3B

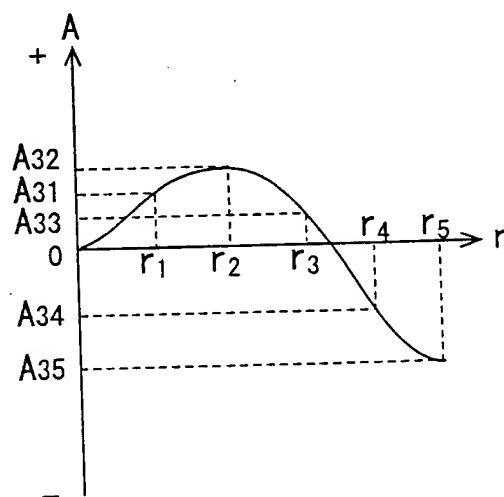


図4

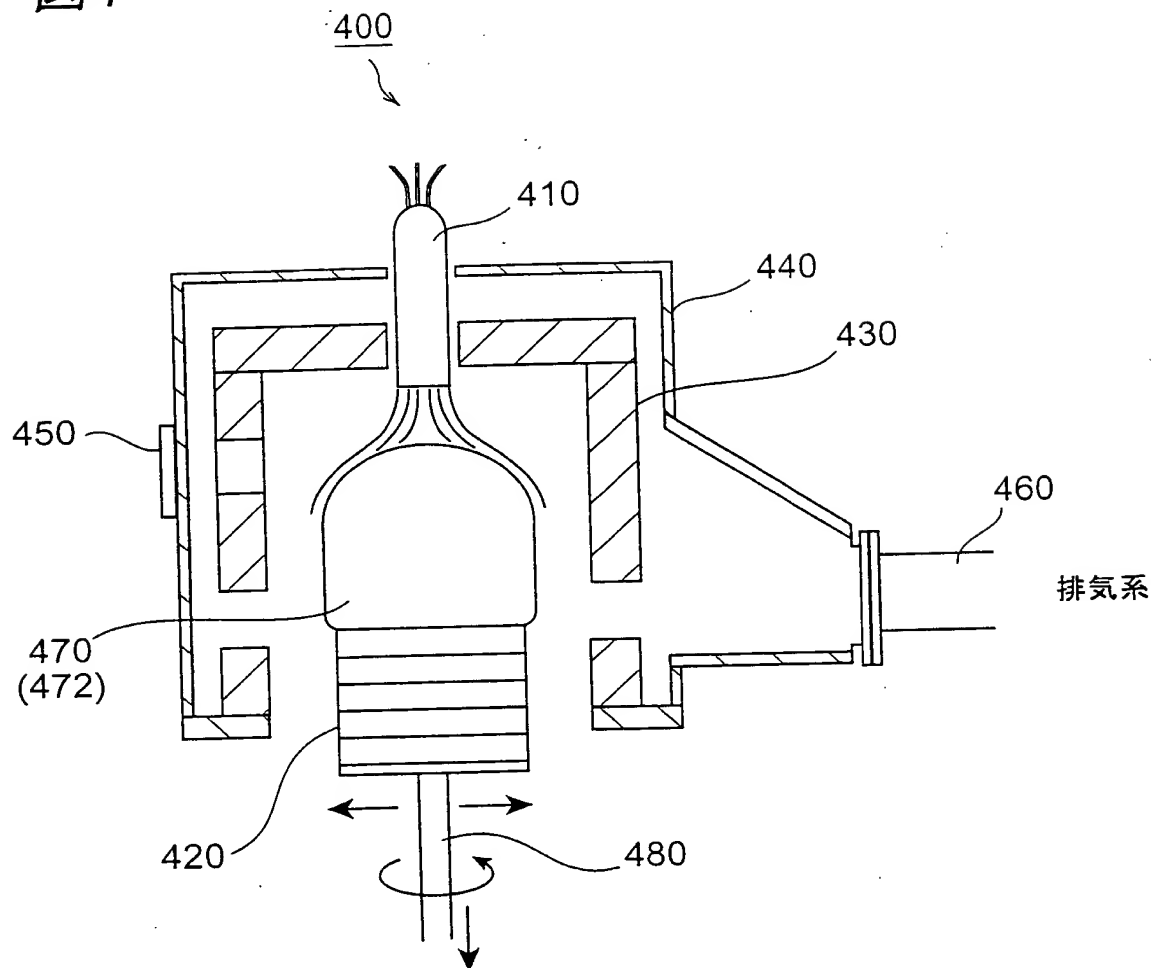


図5

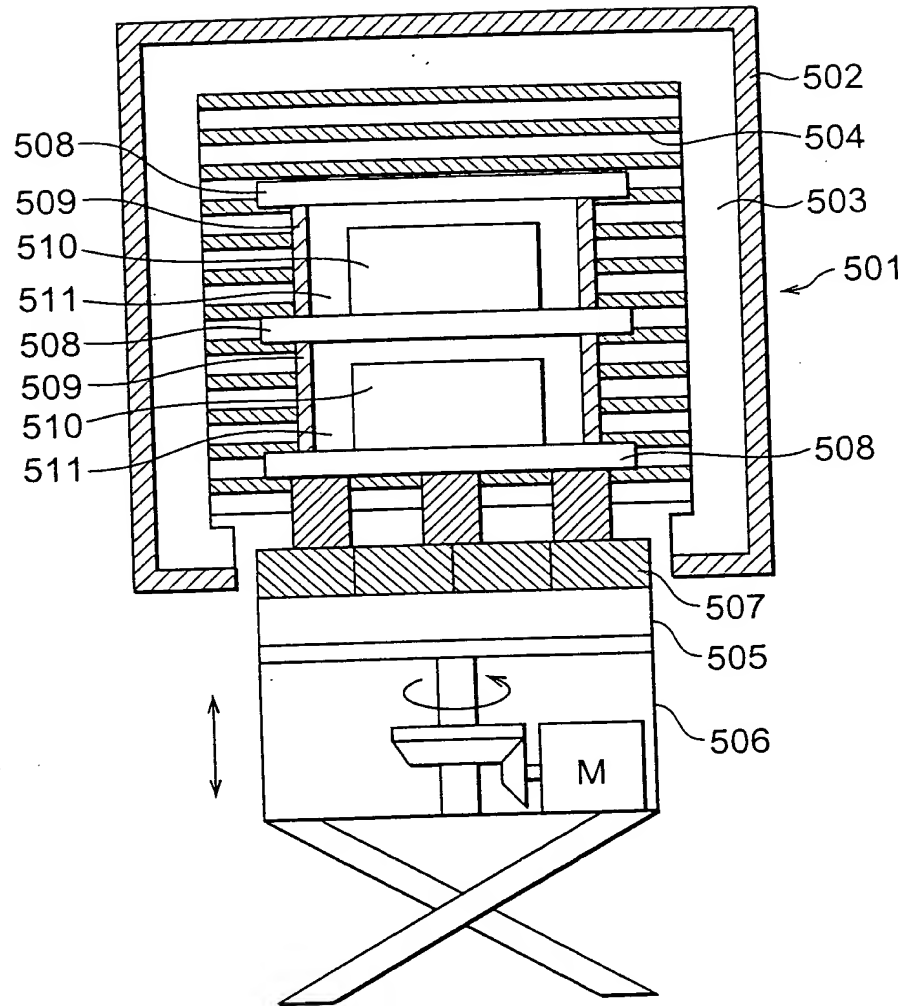


図6A

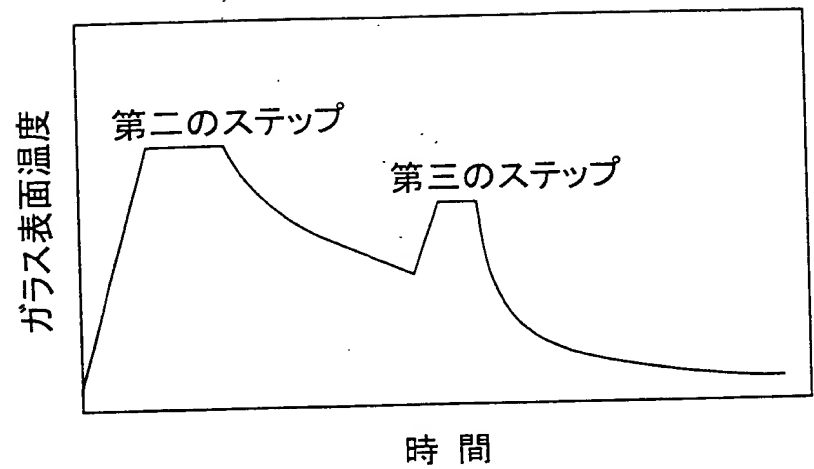


図6B

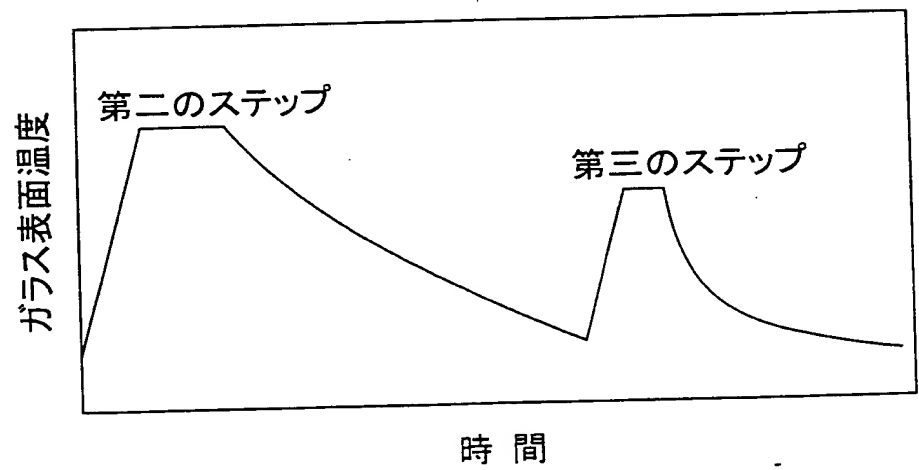


図7A

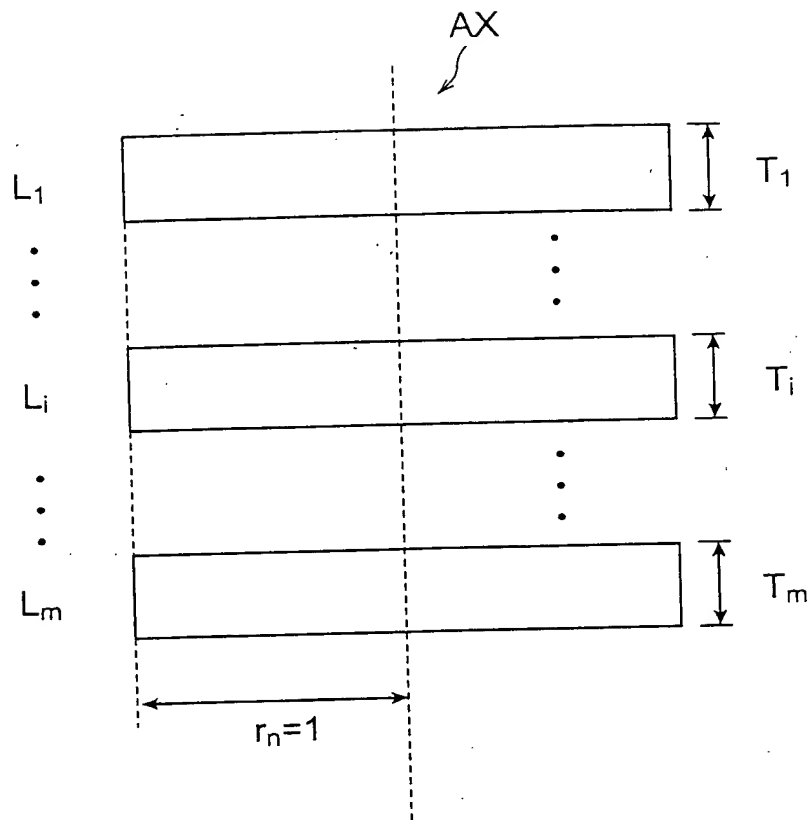


図7B

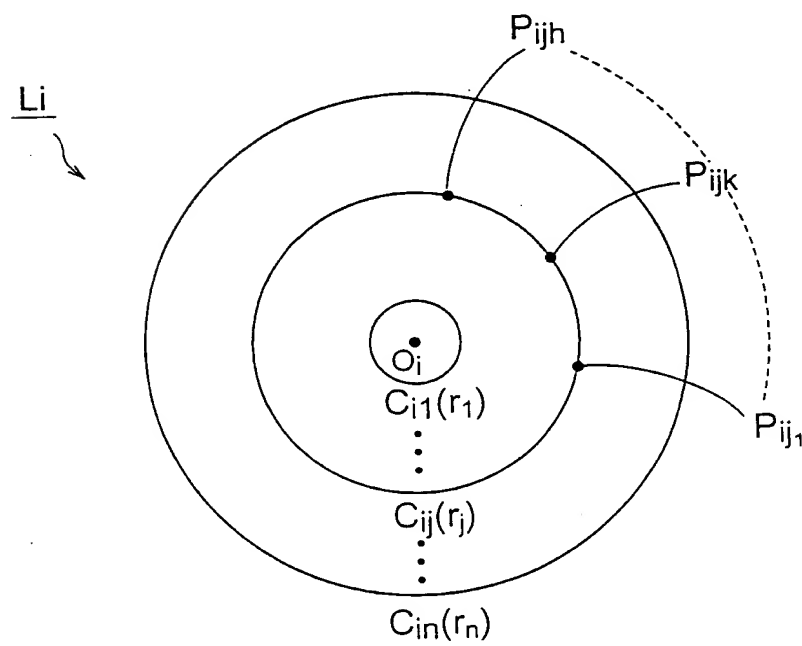


図8

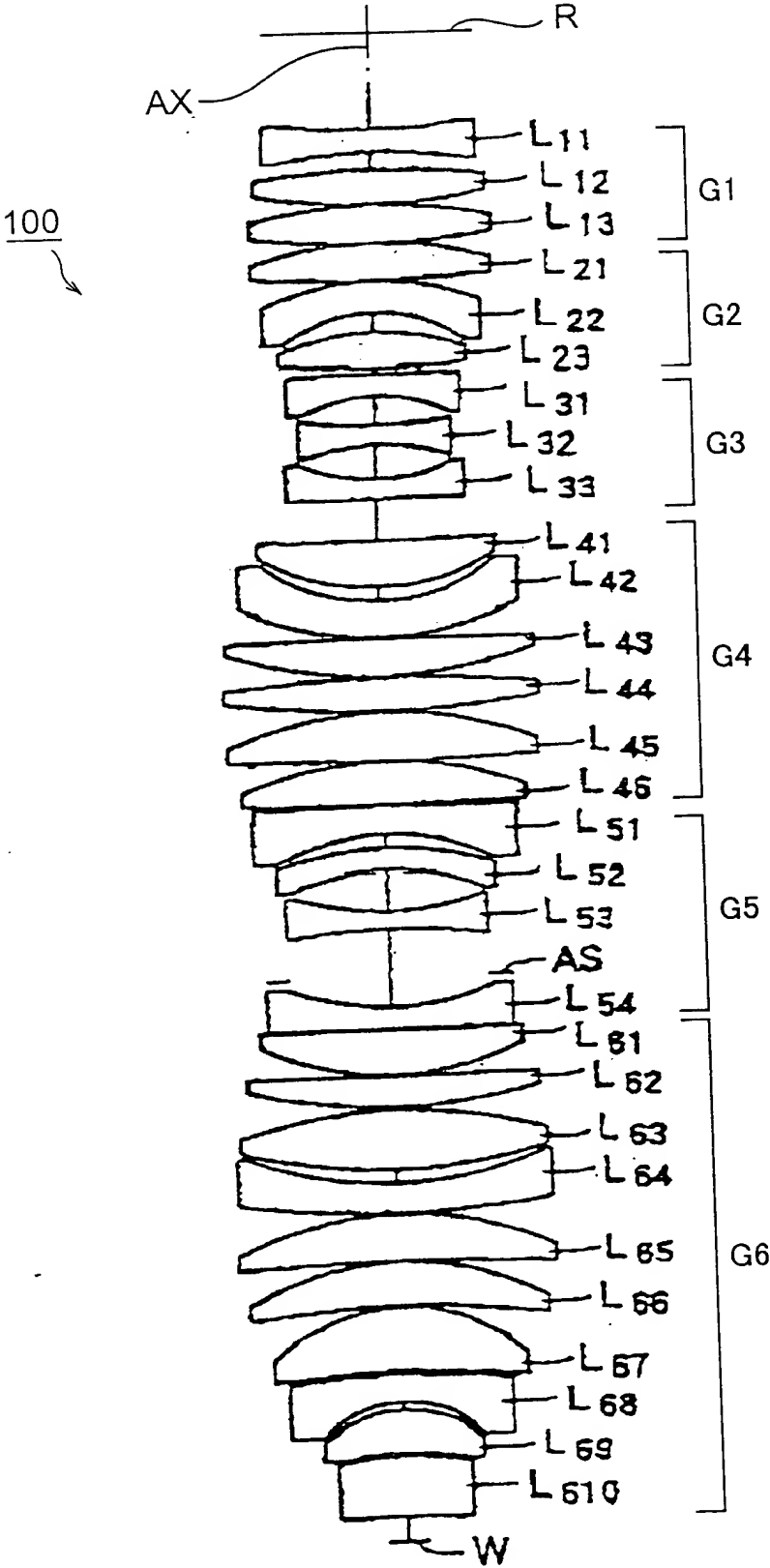
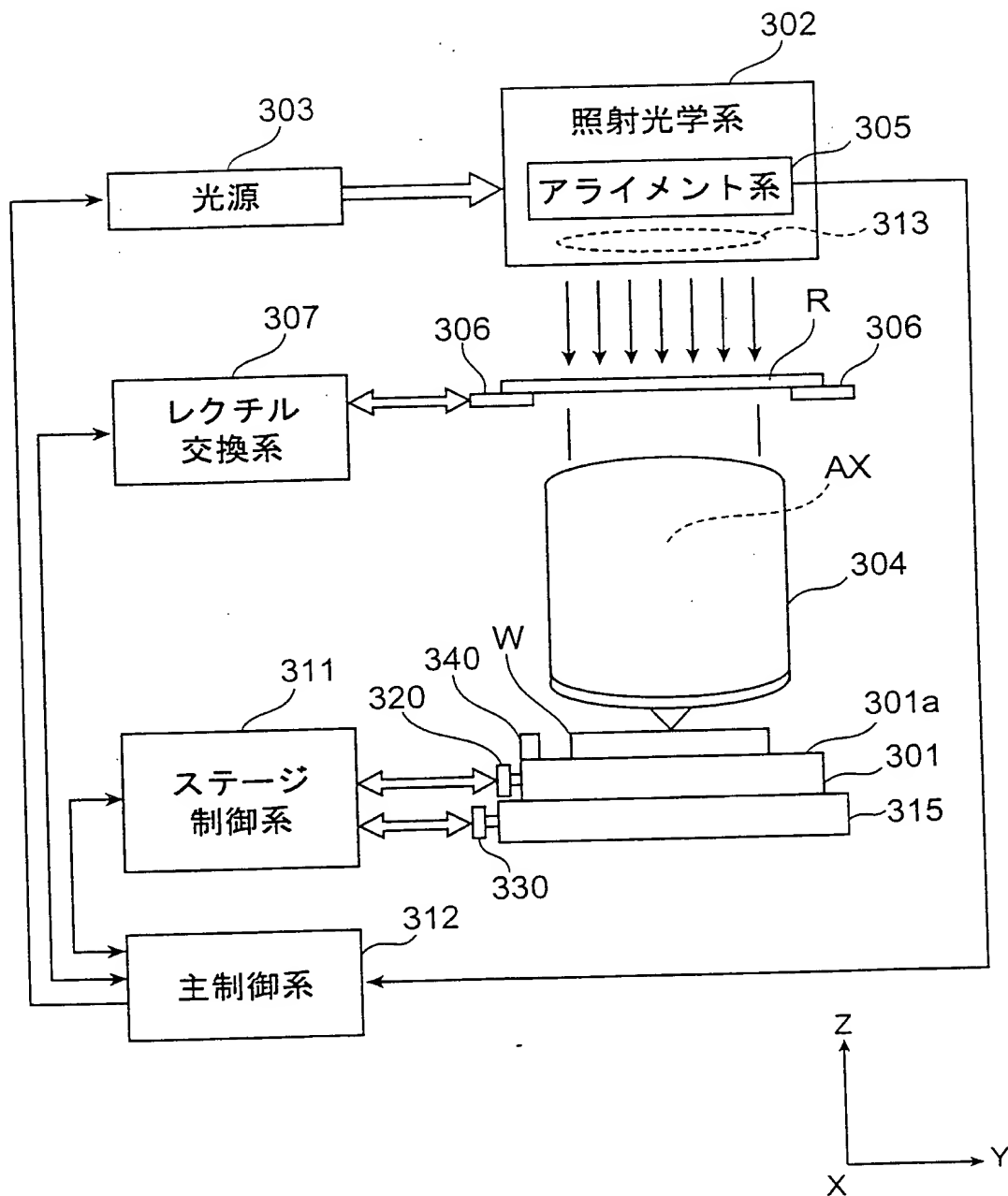
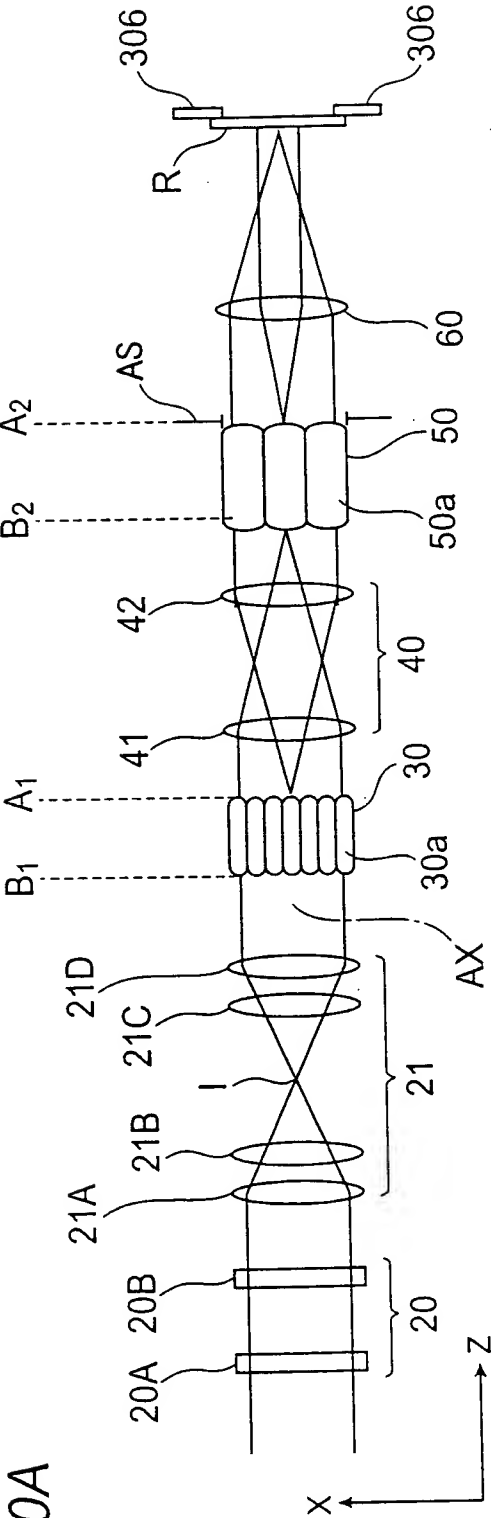




図9



10A



10B

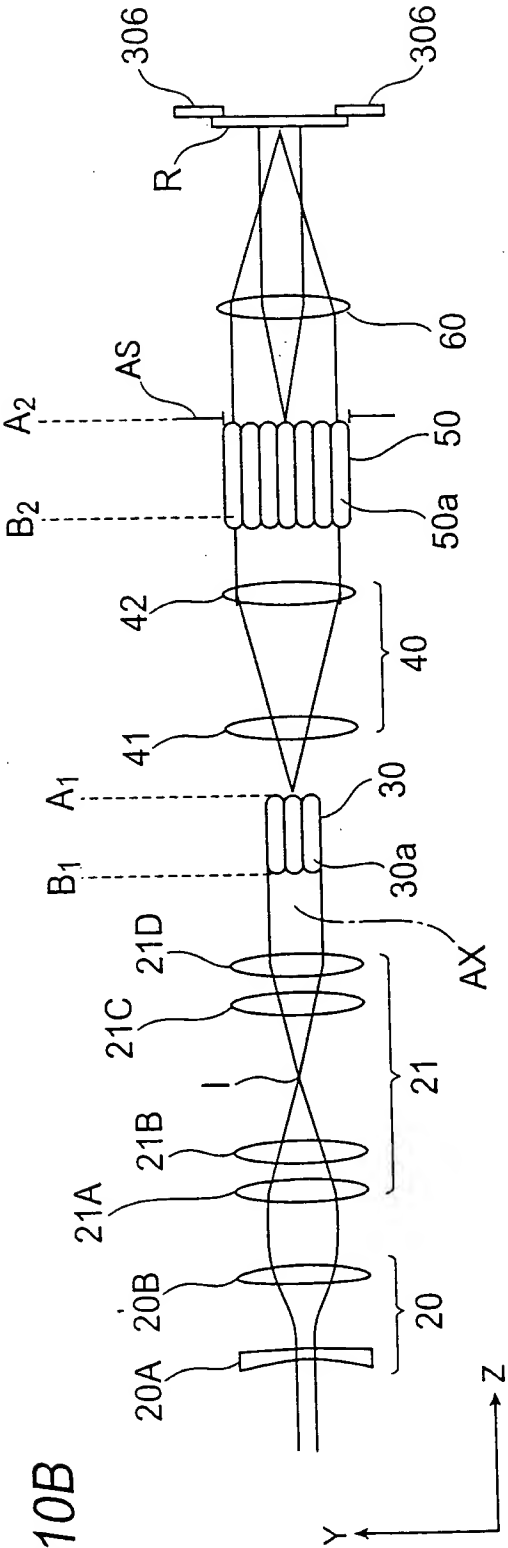


図11A

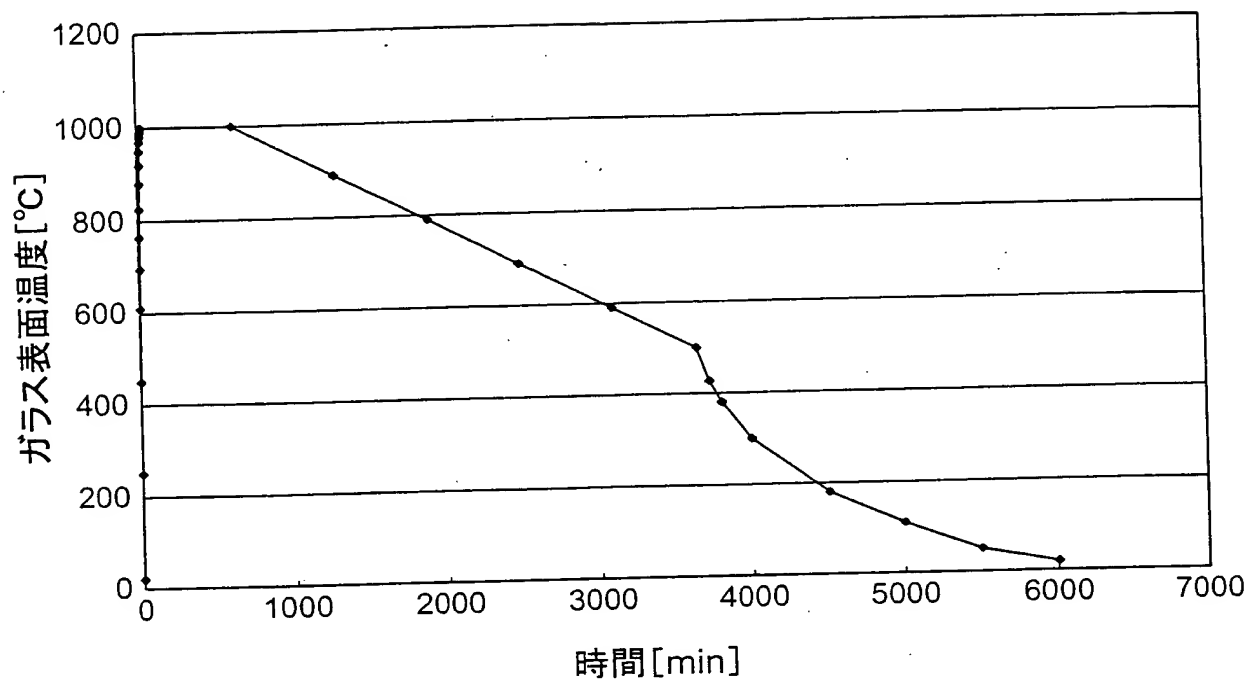


図11B

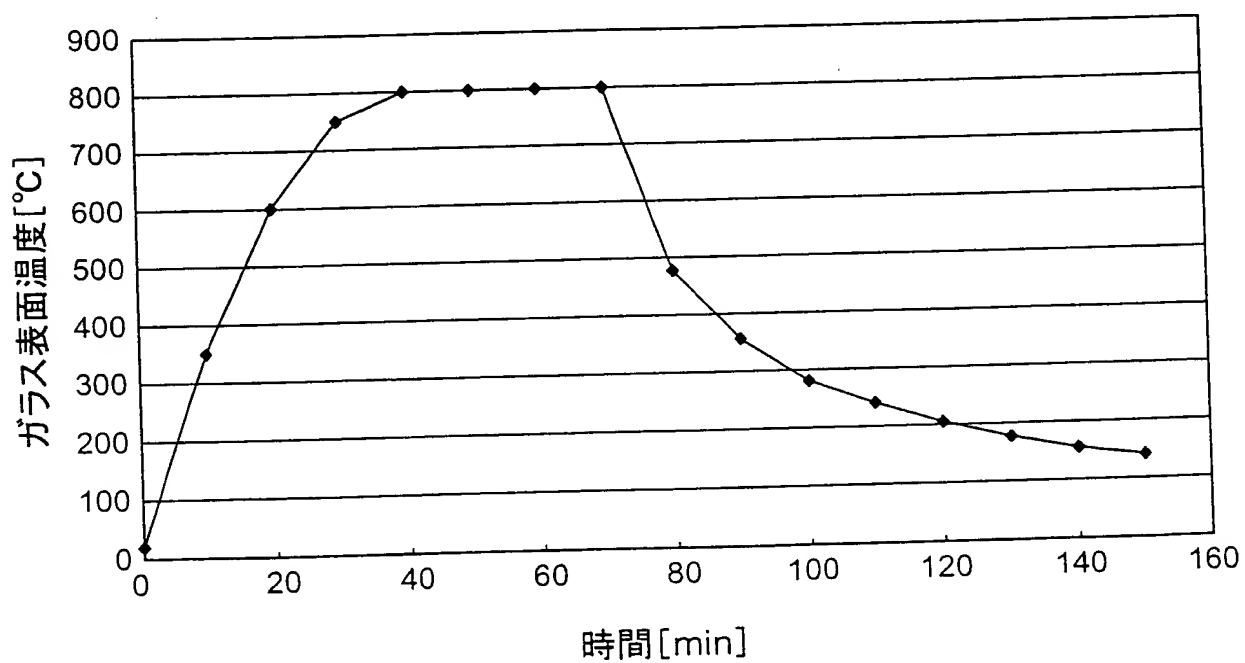


図12A

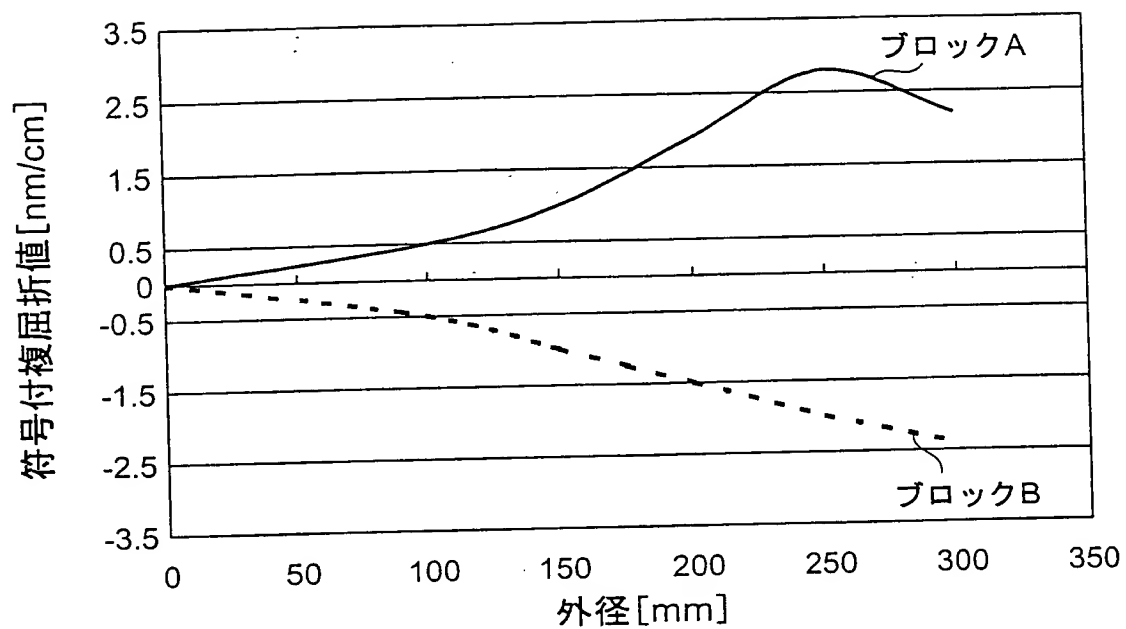


図12B

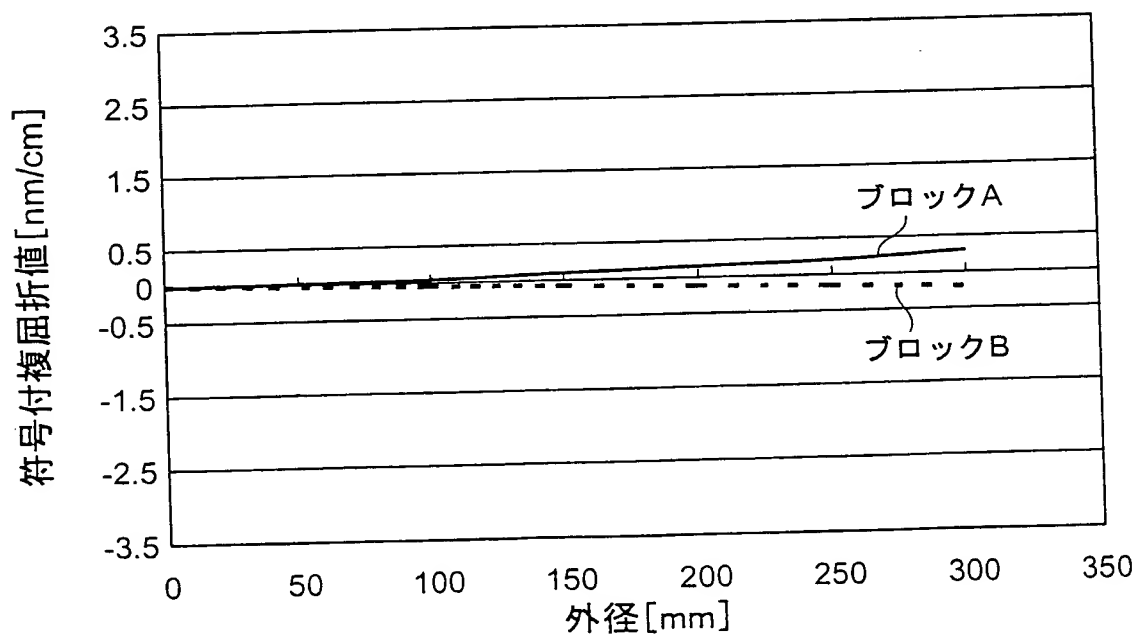


図13

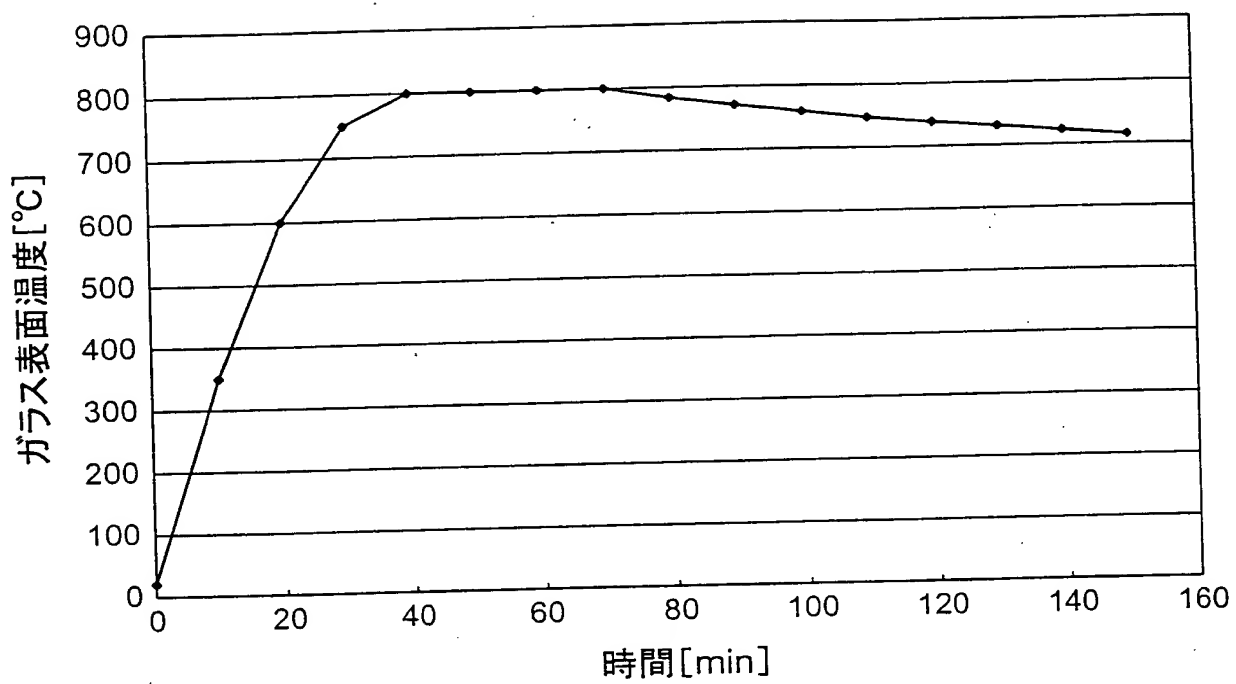


図14A

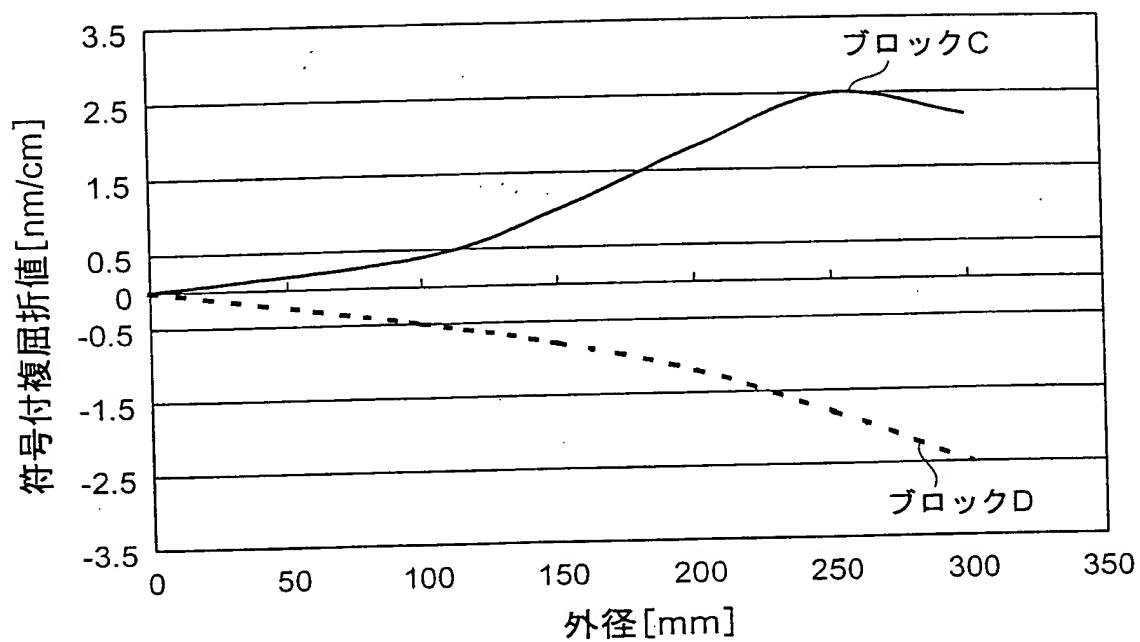


図14B

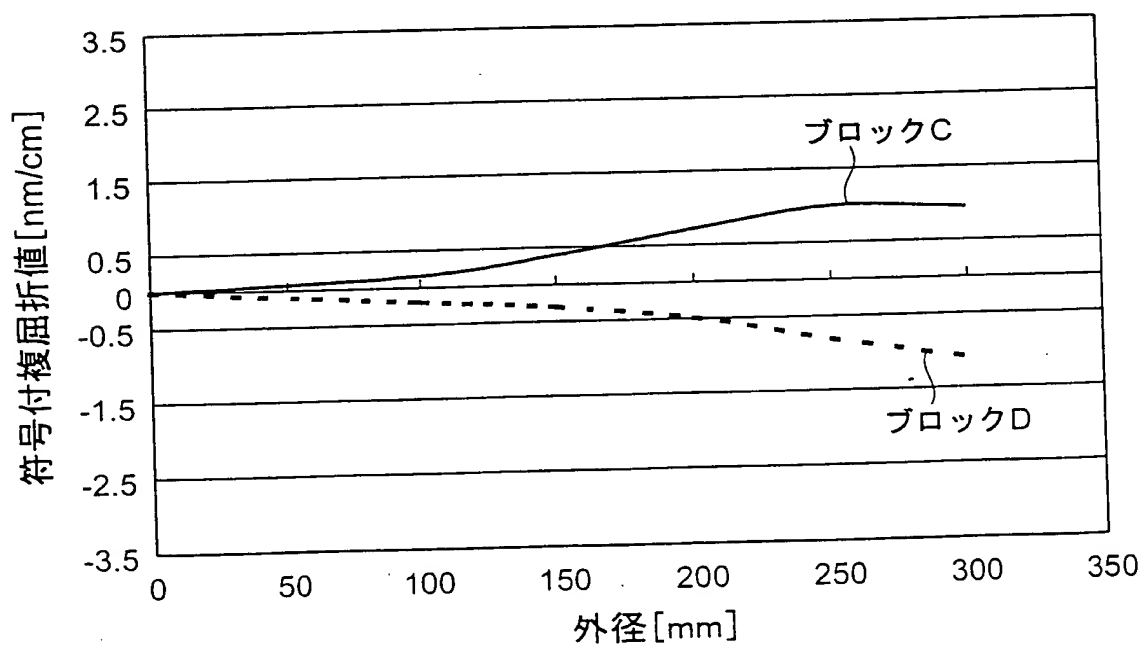


図15

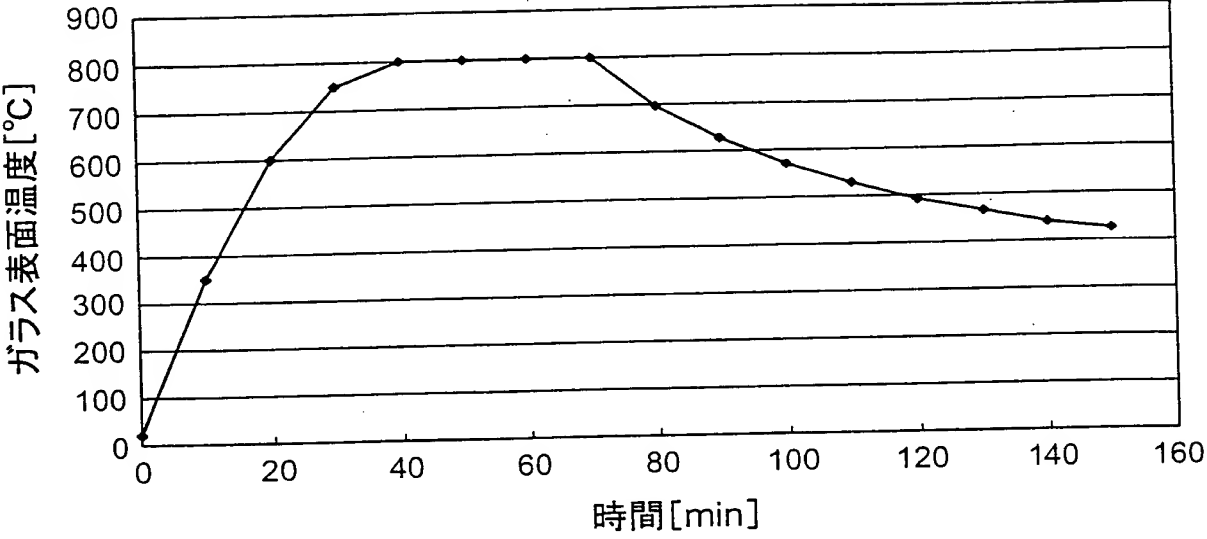


図16A

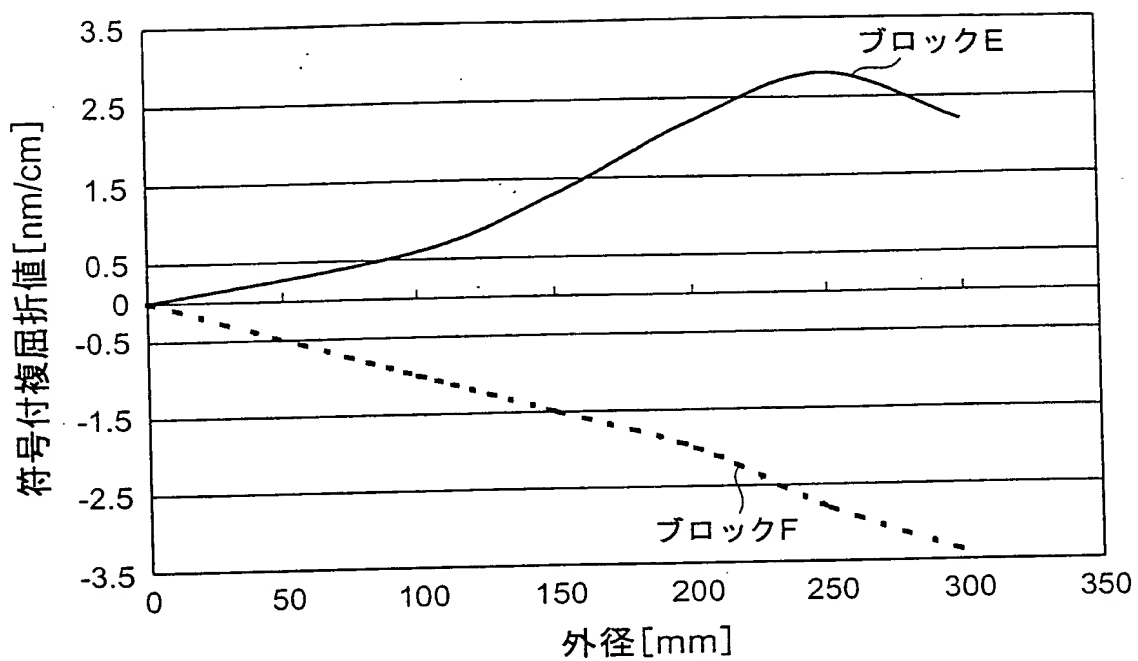


図16B

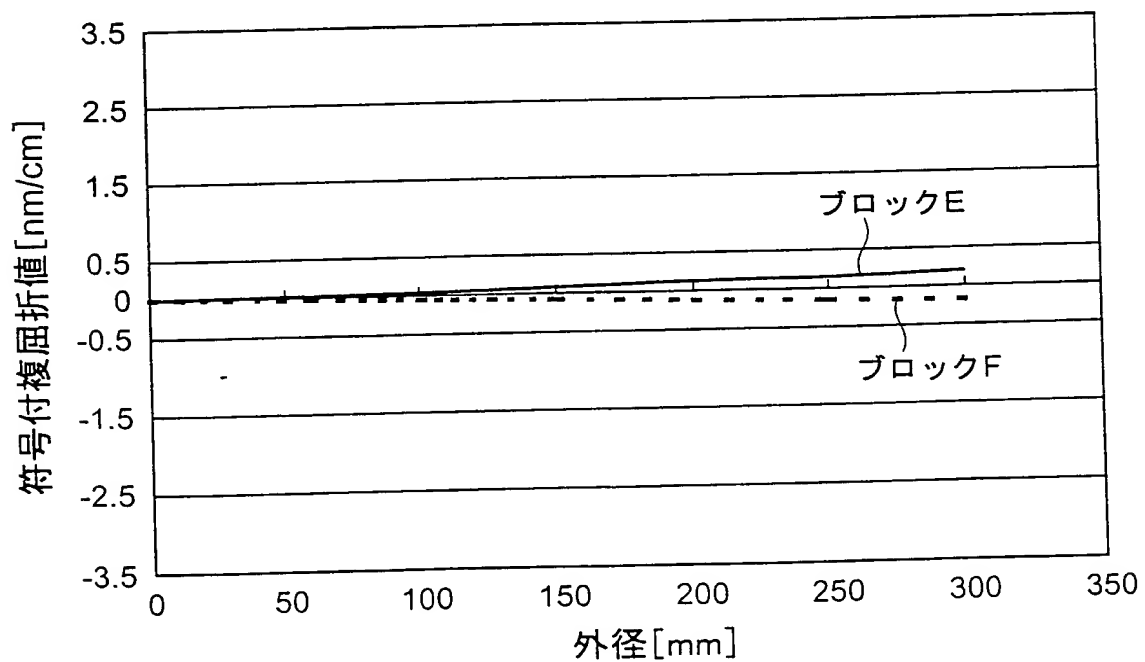




図17A

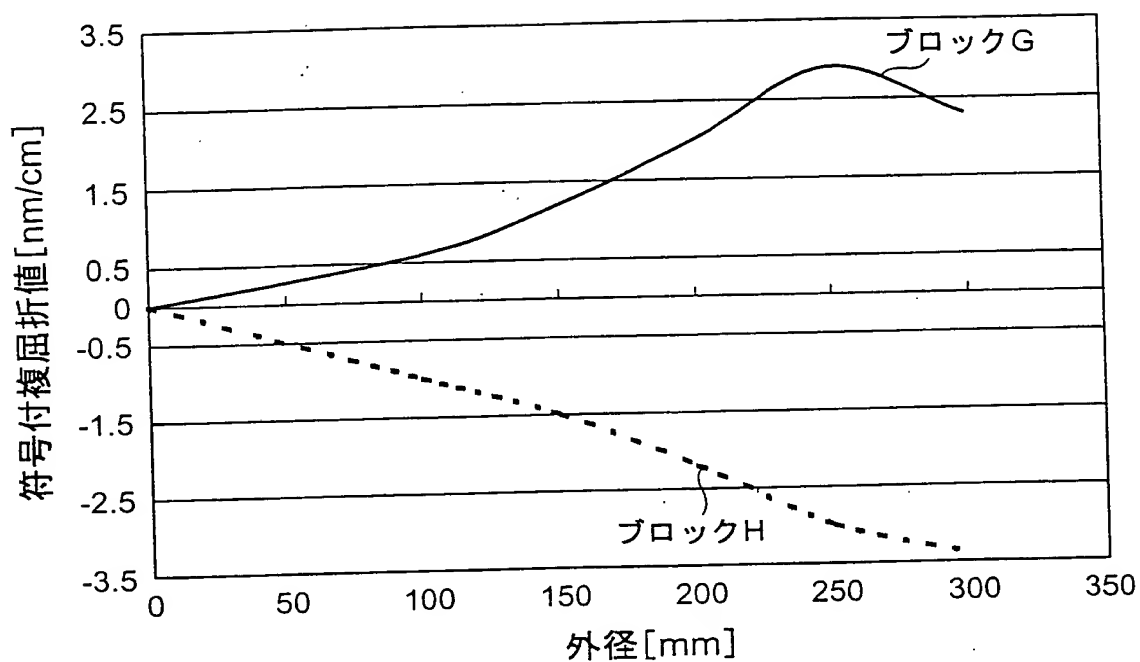


図17B

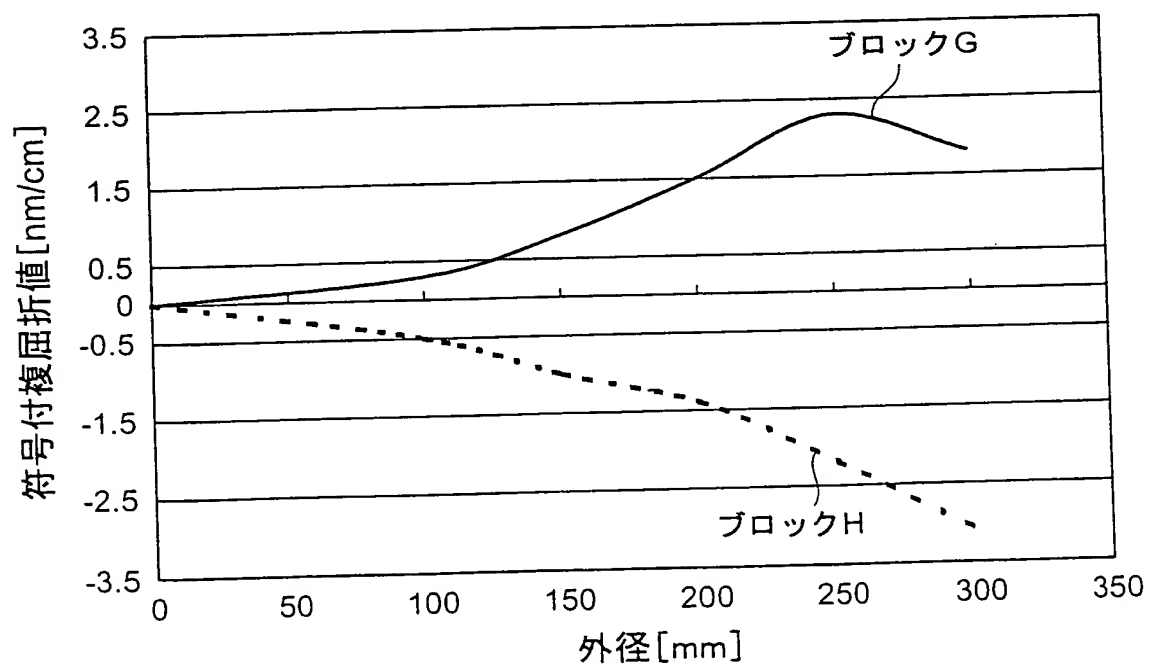


図18

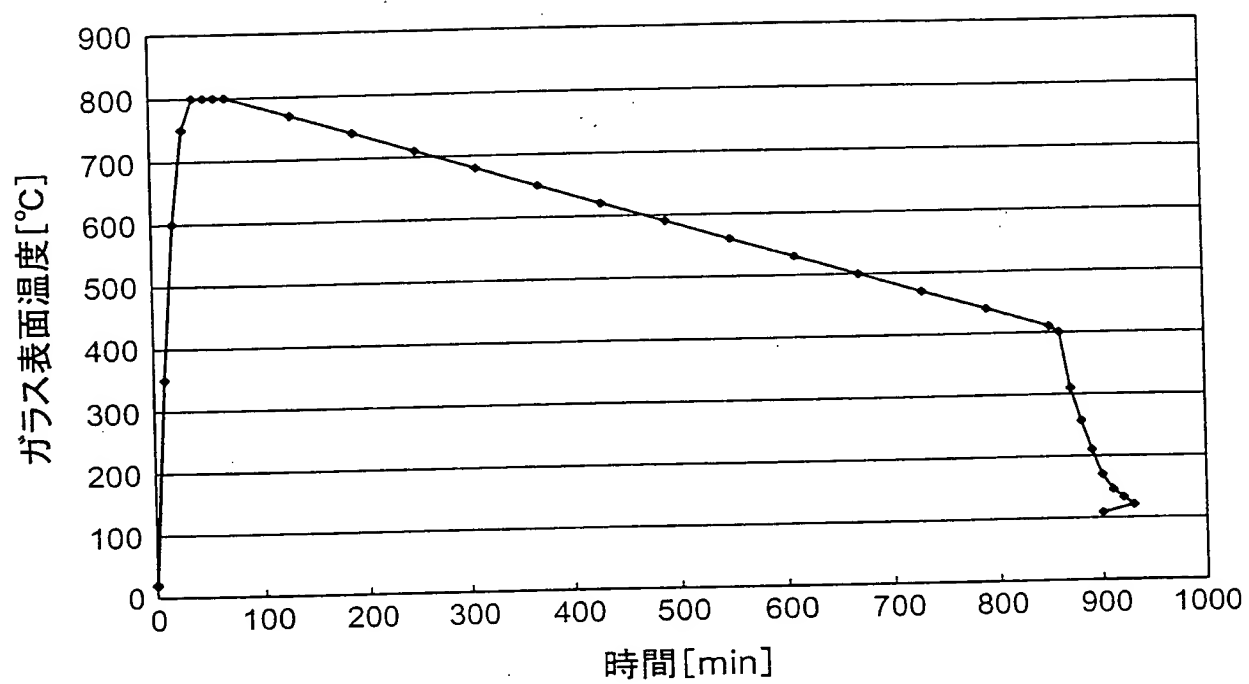


図19A

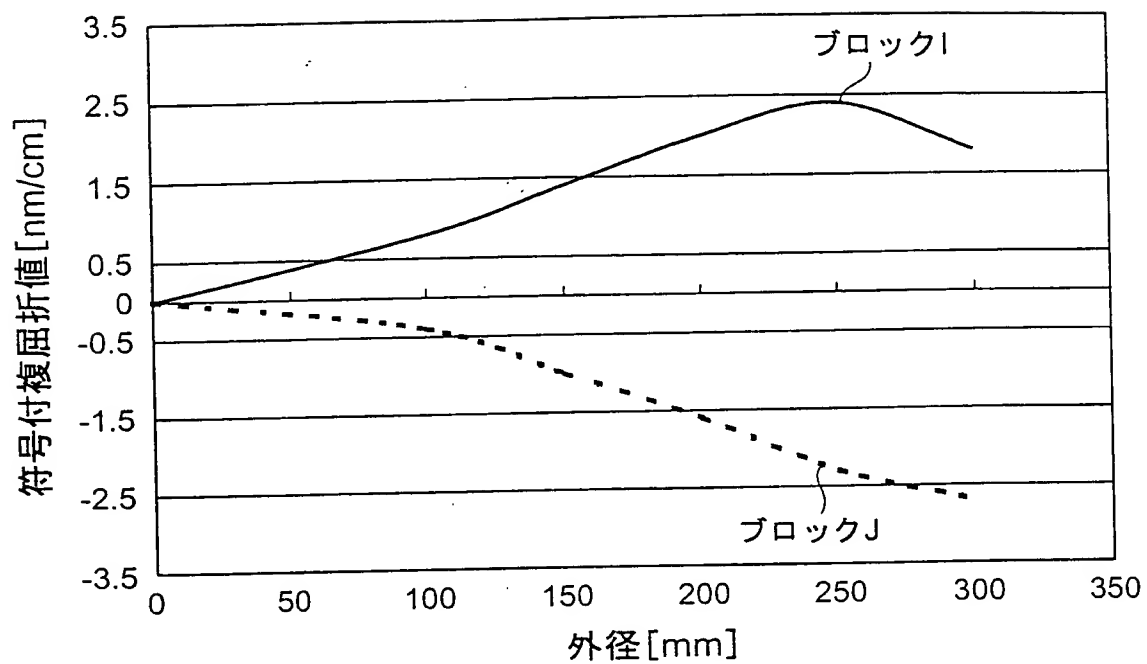


図19B

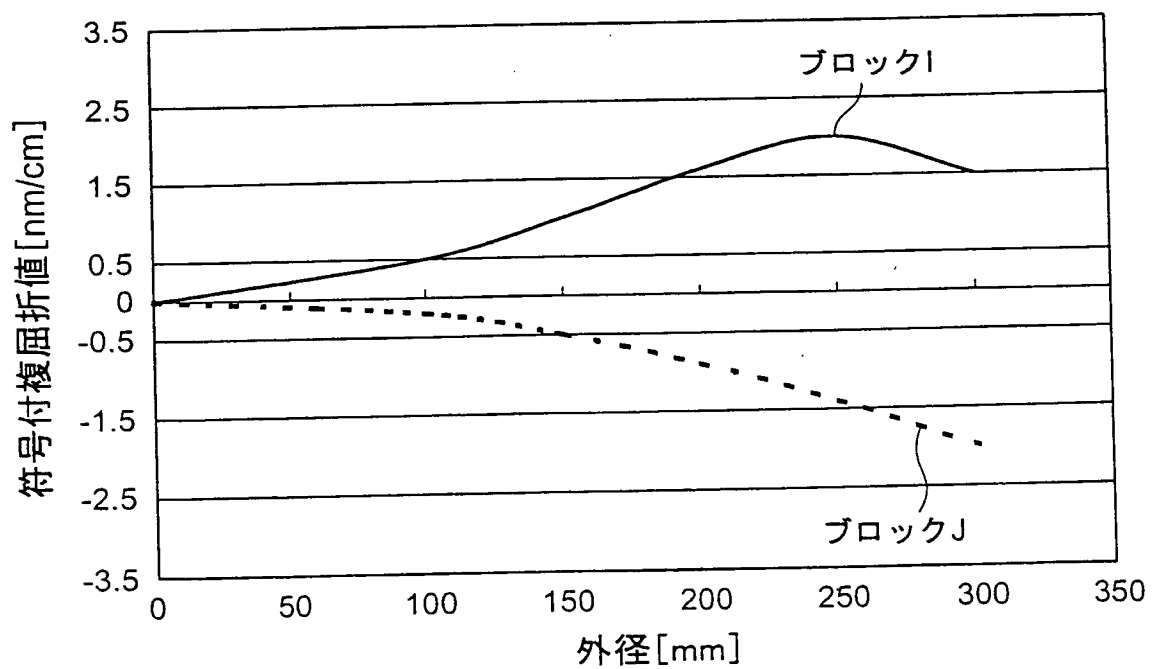


図20

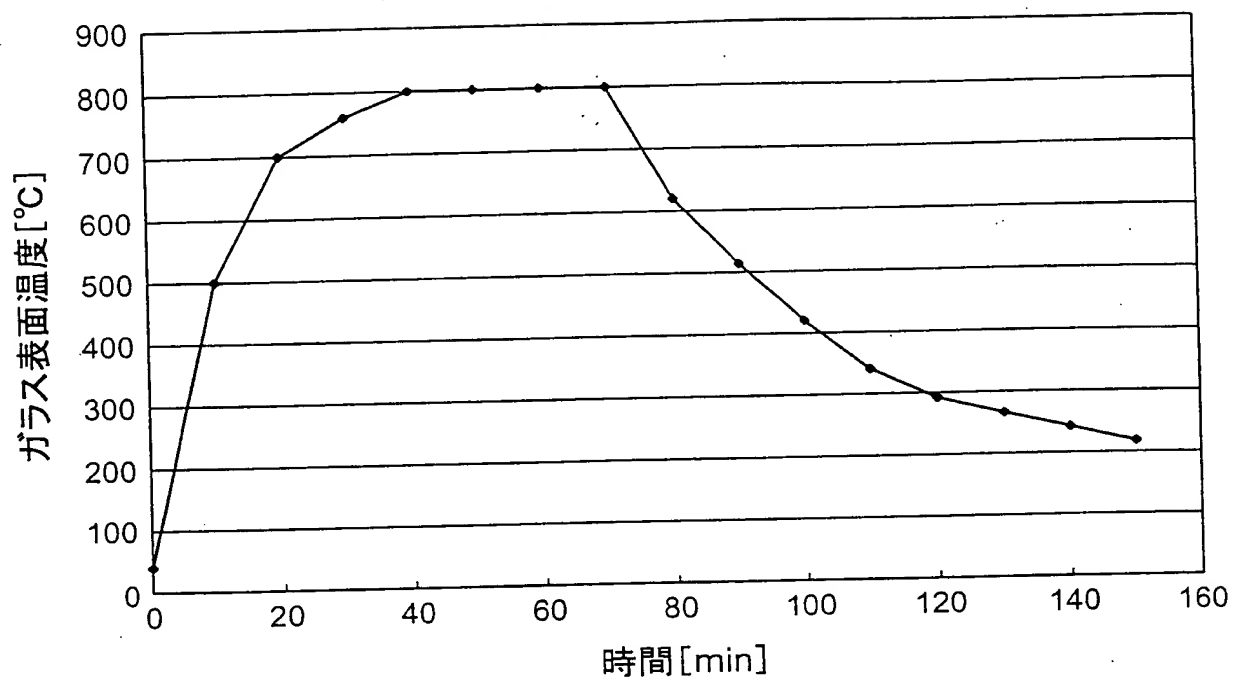


図21A

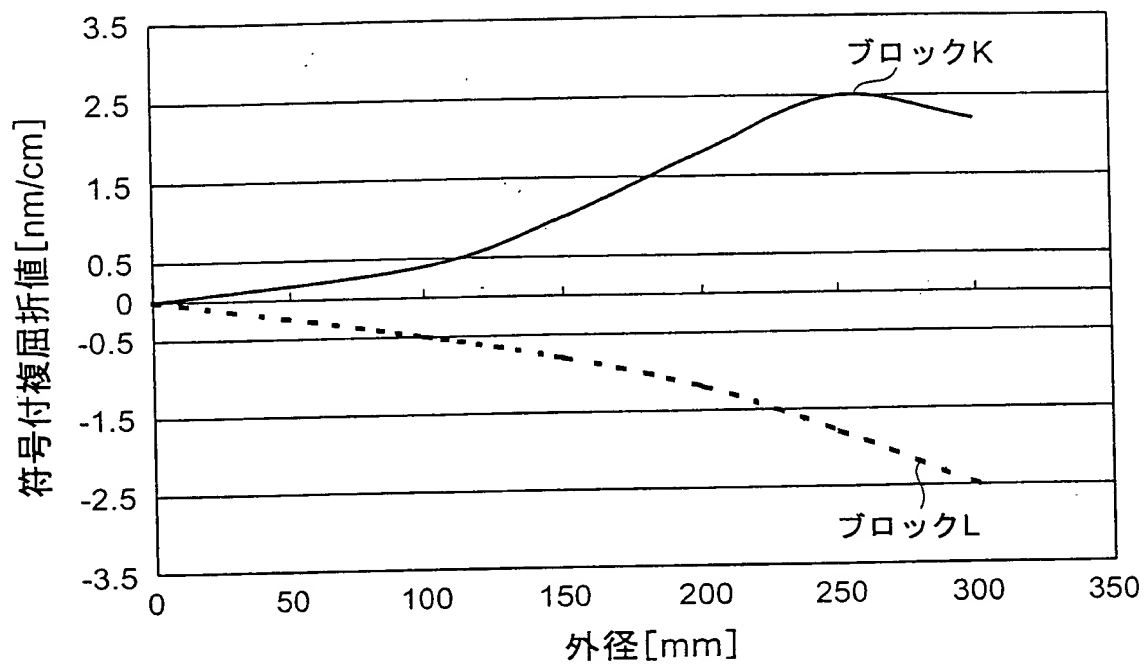


図21B

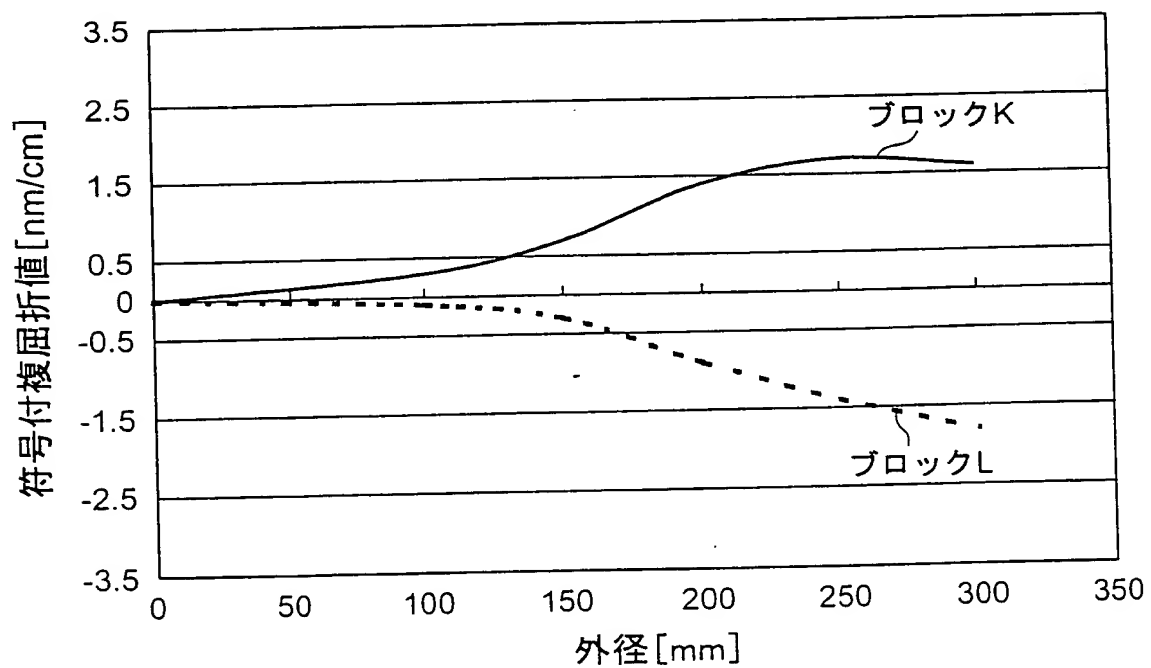


図22A

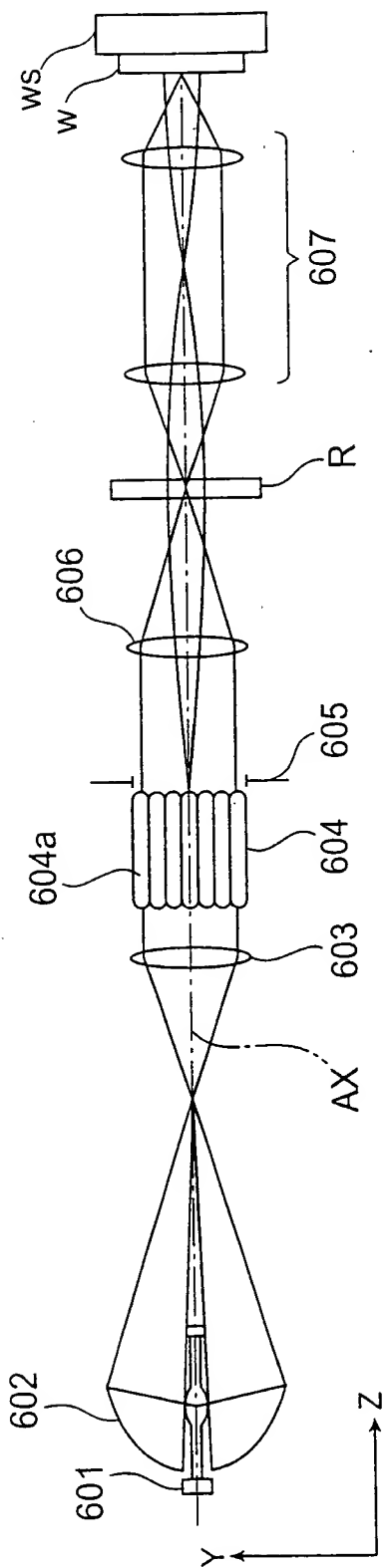


図22B

